



K.S.INSTITUTE OF TECHNOLOGY, BENGALURU – 560109
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Project Exhibition 2017-18

Report:

Project exhibition for final year students along with mini project exhibition was conducted on 10 th May 2018. Inauguration of project exhibition started at 9.30am in AEC lab, Communication lab and LD lab in the esteemed presence of chief Guests along with the Principal, HOD, staff and students of ECE department.

The judge invited to evaluate were

1. Mr.Ganeshan Viswanathan ,
Viswa Jyothi Technologies Pvt Ltd.
Bangalore.
2. Mr.Srinivasan A N
KMD Tiny Chips , Embedded Chips,
Region Head South. Skill India,
Bangalore.

The students exhibited /demonstrated their working models along with the poster. There were 41 teams along with 7 mini project teams, with around 3-4 students in a team. The students explained their projects, its implementation methodology and its applications to the judges. The judges selected best three projects.

Collected the feedback from the judges regarding the conduction of the project exhibition and quality of projects.The judges addressed the students and provided their valuable insights. The exhibition was concluded in the seminar hall and high tea was arranged for all the students and staff of E&C department.



