



K.S. INSTITUTE OF TECHNOLOGY, BANGALORE -109
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
III SEMESTER TIME TABLE FOR THE YEAR 2019 (ODD SEMESTER)

W.E.F. : 29.7.2019

CLASS TEACHER : Mrs. VEDA B

SEC : 'A'

CLASS ROOM : OB LH 201

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.30 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.30 PM 2.25 PM	2.25 PM 3.20 PM	3.20 PM 4.15 PM
MON	NT (18EC32)	ED (18EC33)	T E A	DSD (18EC34)	PI (18EC36)	L U N C H B R E A K	CO (18EC35)	MATHS (18MAT31)	CO (18EC35)
TUE	← ED LAB(18ECL37) - A1 / DSD LAB(18ECL38) -A3 →			MATHS (18MAT31)			ED (18EC33)	NT (18EC32)	DSD (18EC34)
WED	ED (18EC33)	PI (18EC36)	B R E	DSD (18EC34)	MATHS (18MAT31)		NT (18EC32)	KANNADA (18KVK39/49)	T
THU	← ED LAB(18ECL37) - A2 / DSD LAB (18ECL38)-A1 →			CO (18EC35)			DSD (18EC34)	ED (18EC33)	PI (18EC36)
FRI	PI (18EC36)	MATHS (18MAT31)	A K	NT (18EC32)	CO (18EC35)		← ED LAB (18ECL37)- A3 / DSD LAB (18ECL38) -A2 →		

Sub-Code	Subject Name	Faculty Name
18MATDIP 31	Additional Mathematics - I	
18MAT31	Mathematics	Mrs. Jalaja
18EC32	Network Theory	Mrs. Pragati. P
18EC33	Electronic Devices	Mrs. R. Nithya Kumari
18EC34	Digital System Design	Mrs. Veda . B
18EC35	Computer Organization & Architecture	Mrs. Bhargavi Ananth
18EC36	Power Electronics & Instrumentation	Mrs. Pooja S
18ECL37	Electronic Devices & Instrumentation Laboratory	Mrs. Pragati. P, Mrs. Sahana Salagare
18ECL38	Digital System Design Laboratory	Mrs. Veda . B ,Mrs. Pooja S
18KVK39/49	Vyavaharika Kannada (Kannada for communication)/ for non-kannada speaking	
18KAK39/49	Aadalitha Kannada (Kannada for Administration) for students who speak, read and write kannada	

Time Table Co-ordinator

HOD

Principal



K.S. INSTITUTE OF TECHNOLOGY, BANGALORE -109
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
III SEMESTER TIME TABLE FOR THE YEAR 2019 (ODD SEMESTER)

W.E.F. : 29.7.2019

SEC : 'B'

CLASS TEACHER : Mrs. NITHYA KUMARI R

CLASS ROOM : OB LH 203

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.30 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.30 PM 2.25 PM	2.25 PM 3.20 PM	3.20 PM 4.15 PM
MON	MATHS (18MAT31)	DSD (18EC34)	T E A B R E A K	PI (18EC36)	NT (18EC32)	L U N C H B R E A K	← ED LAB(18ECL37) - B1 / DSD LAB (18ECL38) -B3 →		
TUE	PI (18EC36)	ED (18EC33)		CO (18EC35)	NT (18EC32)		DSD (18EC34)	MATHS (18MAT31)	KANNADA (18KVK39/49)
WED	DSD (18EC34)	CO (18EC35)		NT (18EC32)	ED (18EC33)		← ED LAB (18ECL37) - B2 / DSD LAB (18ECL38) -B1 →		
THU	CO (18EC35)	MATHS (18MAT31)		PI (18EC36)	ED (18EC33)		NT (18EC32)	CO (18EC35)	DSD (18EC34)
FRI	← ED LAB(18ECL37) - B3 / DSD LAB(18ECL38) -B2 →			PI (18EC36)	MATHS (18MAT31)		ED (18EC33)	DIP-MATHS (18MATDIP 31)	

Sub-Code	Subject Name	Faculty Name
18MATDIP 31	Additional Mathematics - I	
18MAT31	Mathematics	Mr. Venkataramana
18EC32	Network Theory	Mrs. Pragati. P
18EC33	Electronic Devices	Mrs. R. Nithya Kumari
18EC34	Digital System Design	Mr. B.R.Santhosh Kumar
18EC35	Computer Organization & Architecture	Mrs. Bhargavi Ananth
18EC36	Power Electronics & Instrumentation	Mrs. Shilpa V
18ECL37	Electronic Devices & Instrumentation Laboratory	Mrs. R. Nithya Kumari, Mrs. Shilpa V
18ECL38	Digital System Design Laboratory	Mr. B.R.Santhosh Kumar, Mrs. Veda . B
18KVK39/49	Vyavaharika Kannada (Kannada for communication)/ for non-kannada speaking	
18KAK39/49	Aadalitha Kannada (Kannada for Administration) for students who speak, read and write kannada	

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Principal



K.S. INSTITUTE OF TECHNOLOGY, BANGALORE -109
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
V SEMESTER TIME TABLE FOR THE YEAR 2019 (ODD SEMESTER)

W.E.F. : 29.7.2019

CLASS TEACHER : Mrs. ARUNA RAO B P

SEC : 'A'

CLASS ROOM : OB LH 204

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.30 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.30 PM 2.25 PM	2.25 PM 3.20 PM	3.20 PM 4.15 PM
MON	OS (17EC553)	ITC (17EC54)	T E A B R E A K	M&E (17ES51)	MC(17EC563)/ OOPS(17EC562)	L U N C H B R E A K	← DSP LAB (17ECL57)- A1 / HDL LAB (17ECL58) -A3 →		
TUE	MC(17EC563)/ OOPS(17EC562)	OS (17EC553)		DSP (17EC52)	ITC (17EC54)		HDL (17EC53)	DSP (17EC52)	T
WED	ITC (17EC54)	OS (17EC553)		MC(17EC563)/ OOPS(17EC562)	HDL (17EC53)		← DSP LAB(17ECL57) - A2 / HDL LAB (17ECL58) -A1 →		
THU	← DSP LAB (17ECL57)- A3/ HDL LAB (17ECL58) -A2 →			MC(17EC563)/ OOPS(17EC562)	DSP (17EC52)		M&E (17ES51)	HDL (17EC53)	
FRI	M&E (17ES51)	DSP (17EC52)		A K	HDL (17EC53)		OS (17EC553)	M&E (17ES51)	ITC (17EC54)

Sub-Code	Subject Name	Faculty Name
17ES51	Management and Entrepreneurship Development	Dr. Sangappa.S.B
17EC52	Digital Signal Processing	Mrs. Jayasudha B.S.K
17EC53	Verilog HDL	Mrs. Aruna Rao B.P
17EC54	Information Theory & Coding	Mr. Sunil Kumar.G.R
17EC553	Operating System (Professional Elective-1)	Dr. B.Sudharshan
17EC562	Object Oriented Programming Using C++ (Open Elective-1)	Dr. P Joy Prabhakaran
17EC563	8051 Microcontroller (Open Elective-1)	Mr. B.R.Santhosh Kumar
17ECL57	DSP Lab	Mrs. Jayasudha B.S.K, Mr. Christo Jain
17ECL58	HDL Lab	Mrs. Aruna Rao B.P, Mr. Sunil Kumar.G.R

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K.S. INSTITUTE OF TECHNOLOGY, BANGALORE -109
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
V SEMESTER TIME TABLE FOR THE YEAR 2019 (ODD SEMESTER)

W.E.F. : 29.7.2019

CLASS TEACHER : Mrs. JAYASUDHA B S K

SEC : 'B'

CLASS ROOM : OB LH 205

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.30 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.30 PM 2.25 PM	2.25 PM 3.20 PM	3.20 PM 4.15 PM
MON	DSP (17EC52)	HDL (17EC53)	T E A B R E A K	OS (17EC553)	MC(17EC563)/ OOPS(17EC562)	L U N C H B R E A K	M&E (17ES51)	OS (17EC553)	T
TUE	MC(17EC563)/ OOPS(17EC562)	ITC (17EC54)		HDL (17EC53)	ITC (17EC54)		← DSP LAB (17ECL57)- B1 / HDL LAB(17ECL58) -B3 →		
WED	HDL (17EC53)	DSP (17EC52)		MC(17EC563)/ OOPS(17EC562)	DSP (17EC52)		OS (17EC553)	M&E (17ES51)	T
THU	ITC (17EC54)	M&E (17ES51)		OS (17EC553)	MC(17EC563)/ OOPS(17EC562)		← DSP LAB (17ECL57)- B2 / HDL LAB(17ECL58) -B1 →		
FRI	← DSP LAB(17ECL57) - B3 / HDL LAB(17ECL58) -B2 →			DSP (17EC52)	HDL (17EC53)		ITC (17EC54)	M&E (17ES51)	

Sub-Code	Subject Name	Faculty Name
17ES51	Management and Entrepreneurship Development	Dr. Sangappa.S.B
17EC52	Digital Signal Processing	Mrs. Jayasudha B.S.K
17EC53	Verilog HDL	Mrs. Aruna Rao B.P
17EC54	Information Theory & Coding	Mr. Praveen.A
17EC553	Operating System (Professional Elective-1)	Dr. B.Sudharshan
17EC562	Object Oriented Programming Using C++ (Open Elective-1)	Dr. P Joy Prabhakaran
17EC563	8051 Microcontroller (Open Elective-1)	Mr. B.R.Santhosh Kumar
17ECL57	DSP Lab	Mrs.Sangeetha.V,Dr. B.Sudharshan
17ECL58	HDL Lab	Mr. Sunil Kumar.G.R, Dr. B Surekha

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K.S. INSTITUTE OF TECHNOLOGY, BANGALORE -109
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
V SEMESTER TIME TABLE FOR THE YEAR 2019 (ODD SEMESTER)

W.E.F. : 29.7.2019

CLASS TEACHER : Mrs. ARUNA RAO B P

SEC : 'A'

Parallel Scheme

CLASS ROOM : OB LH 204

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.30 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.30 PM 2.25 PM	2.25 PM 3.20 PM	3.20 PM 4.15 PM
MON	OS (15EC553)	ITC (15EC54)	T E A B R E A K	M&E (15ES51)	MC(15EC563)/ OOPS(15EC562)	L U N C H B R E A K	← DSP LAB (15ECL57)- A1 / HDL LAB (15ECL58) -A3 →		
TUE	MC(15EC563)/ OOPS(15EC562)	OS (15EC553)		DSP (15EC52)	ITC (15EC54)		HDL (15EC53)	DSP (15EC52)	T
WED	ITC (15EC54)	OS (15EC553)		MC(15EC563)/ OOPS(15EC562)	HDL (15EC53)		← DSP LAB(15ECL57) - A2 / HDL LAB (15ECL58) -A1 →		
THU	← DSP LAB (15ECL57)- A3/ HDL LAB (15ECL58) -A2 →			MC(15EC563)/ OOPS(15EC562)	DSP (15EC52)		M&E (15ES51)	HDL (15EC53)	
FRI	M&E (15ES51)	DSP (15EC52)		A K	HDL (15EC53)		OS (15EC553)	M&E (15ES51)	ITC (15EC54)

Sub-Code	Subject Name	Faculty Name
15ES51	Management and Entrepreneurship Development	Dr. Sangappa.S.B
15EC52	Digital Signal Processing	Mrs. Jayasudha B.S.K
15EC53	Verilog HDL	Mrs. Aruna Rao B.P
15EC54	Information Theory & Coding	Mr. Sunil Kumar.G.R
15EC553	Operating System (Professional Elective-1)	Dr. B.Sudharshan
15EC562	Object Oriented Programming Using C++ (Open Elective-1)	Dr. P Joy Prabhakaran
15EC563	8051 Microcontroller (Open Elective-1)	Mr. B.R.Santhosh Kumar
15ECL57	DSP Lab	Mrs. Jayasudha B.S.K, Mr. Christo Jain
15ECL58	HDL Lab	Mrs. Aruna Rao B.P, Mr. Sunil Kumar.G.R

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K.S. INSTITUTE OF TECHNOLOGY, BANGALORE -109
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
V SEMESTER TIME TABLE FOR THE YEAR 2019 (ODD SEMESTER)

W.E.F. : 29.7.2019

CLASS TEACHER : Mrs. JAYASUDHA B S K

SEC : 'B'

Parallel Scheme

CLASS ROOM : OB LH 205

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.30 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.30 PM 2.25 PM	2.25 PM 3.20 PM	3.20 PM 4.15 PM
MON	DSP (15EC52)	HDL (15EC53)	T E A B R E A K	OS (15EC553)	MC(15EC563)/ OOPS(15EC562)	L U N C H B R E A K	M&E (15ES51)	OS (15EC553)	T
TUE	MC(15EC563)/ OOPS(15EC562)	ITC (15EC54)		HDL (15EC53)	ITC (15EC54)		← DSP LAB (15ECL57)- B1 / HDL LAB(15ECL58) -B3 →		
WED	HDL (15EC53)	DSP (15EC52)		MC(15EC563)/ OOPS(15EC562)	DSP (15EC52)		OS (15EC553)	M&E (15ES51)	T
THU	ITC (15EC54)	M&E (15ES51)		OS (15EC553)	MC(15EC563)/ OOPS(15EC562)		← DSP LAB (15ECL57)- B2 / HDL LAB(15ECL58) -B1 →		
FRI	← DSP LAB(15ECL57) - B3 / HDL LAB(15ECL58) -B2 →			DSP (15EC52)	HDL (15EC53)		ITC (15EC54)	M&E (15ES51)	

Sub-Code	Subject Name	Faculty Name
15ES51	Management and Entrepreneurship Development	Dr. Sangappa.S.B
15EC52	Digital Signal Processing	Mrs. Jayasudha B.S.K
15EC53	Verilog HDL	Mrs. Aruna Rao B.P
15EC54	Information Theory & Coding	Mr. Praveen.A
15EC553	Operating System (Professional Elective-1)	Dr. B.Sudharshan
15EC562	Object Oriented Programming Using C++ (Open Elective-1)	Dr. P Joy Prabhakaran
15EC563	8051 Microcontroller (Open Elective-1)	Mr. B.R.Santhosh Kumar
15ECL57	DSP Lab	Mrs.Sangeetha.V,Dr. B.Sudharshan
15ECL58	HDL Lab	Mr. Sunil Kumar.G.R, Dr. B Surekha

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K.S. INSTITUTE OF TECHNOLOGY, BANGALORE -109
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
V SEMESTER TIME TABLE FOR THE YEAR 2019 (ODD SEMESTER)

W.E.F. : 29.7.2019

CLASS TEACHER : Mrs. JAYASUDHA B S K

SEC : 'B'

Parallel Scheme

CLASS ROOM : OB LH 205

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.30 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.30 PM 2.25 PM	1.30 PM 2.25 PM	2.25 PM 3.20 PM
MON	DSP (17EC52)	HDL (17EC53)	T E A B R E A K	OS (17EC553)	MC(17EC563)/ OOPS(17EC562)	L U N C H B R E A K	M&E (17ES51)	OS (17EC553)	T
TUE	MC(17EC563)/ OOPS(17EC562)	ITC (17EC54)		HDL (17EC53)	ITC (17EC54)		← DSP LAB (17ECL57)- B1 / HDL LAB(17ECL58) -B3 →		
WED	HDL (17EC53)	DSP (17EC52)		MC(17EC563)/ OOPS(17EC562)	DSP (17EC52)		OS (17EC553)	M&E (17ES51)	T
THU	ITC (17EC54)	M&E (17ES51)		OS (17EC553)	MC(17EC563)/ OOPS(17EC562)		← DSP LAB (17ECL57)- B2 / HDL LAB(17ECL58) -B1 →		
FRI	← DSP LAB(17ECL57) - B3 / HDL LAB(17ECL58) -B2 →			DSP (17EC52)	HDL (17EC53)		ITC (17EC54)	M&E (17ES51)	

Sub-Code	Subject Name	Faculty Name
10AL51	Management and Entrepreneurship	Dr. Sangappa.S.B
10EC52	Digital Signal Processing	Mrs. Jayasudha B.S.K
10EC53	Analog Communication	Mrs. Sangeetha V
10EC54	Microwaves and Radar	Dr. Surekha
10EC55	Information Theory & Coding	Mr. Praveen.A
10EC56	Fundamentals of CMOS VLSI	Mr. Praveen.A
10ECL57	DSP Lab	Mrs.Sangeetha.V,Dr. B.Sudharshan
10ECL58	Analog Communication Lab + LIC Lab	Mr. Saleem S Tevaramani

Time Table Co-ordinator

HOD

Principal



K.S. INSTITUTE OF TECHNOLOGY, BANGALORE -109
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
VII SEMESTER TIME TABLE FOR THE YEAR 2019 (ODD SEMESTER)

W.E.F. : 21.8.2019

CLASS TEACHER : Mrs. SANGEETHA V

SEC : 'A'

CLASS ROOM : OB LH 310

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.30 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.30 PM 2.25 PM	2.25 PM 3.20 PM	3.20 PM 4.15 PM
MON	CRYPT (15EC744)	MWA (15EC71)	T E A	PE (15EC73)	DIP (15EC72)	L U N C H B R E A K	← ADC LAB (15ECL76)- A1/ VLSI LAB(15ECL77) -A3 →		
TUE	MWA (15EC71)	CRYPT (15EC744)		SC (15EC755)	DIP (15EC72)		← PROJECT WORK PHASE - I (15ECP78) →		
WED	← ADC LAB(15ECL76) - A2/VLSI LAB(15ECL77) -A1 →			PE (15EC73)			CRYPT (15EC744)	SC (15EC755)	T
THU	CRYPT (15EC744)	MWA (15EC71)	B R E A K	SC (15EC755)	DIP (15EC72)		← ADC LAB (15ECL76) - A3 / VLSI LAB(15ECL77) -A2 →		
FRI	DIP (15EC72)	CRYPT (15EC744)		PE (15EC73)	SC (15EC755)		PE (15EC73)	MWA (15EC71)	T

Sub-Code	Subject Name	Faculty Name
15EC71	Microwave and Antennas	Dr. B Surekha
15EC72	Digital Image Processing	Mr. Saleem S Tevaramani
15EC73	Power Electronics	Mr. Christo Jain
15EC744	Cryptography (Professional Elective-3)	Dr. P.N Sudha
15EC755	Satellite Communication (Professional Elective-4)	Mrs.Sangeetha.V
15ECL76	Advanced Communication Lab	Mrs. Sahana Salagare, Mr. Sampath Kumar S, Mr. Saleem S Tevaramani
15ECL77	VLSI Lab	Mr. Praveen.A, Mrs. Shilpa, Mrs. Pooja S
15ECP78	Project Work Phase-I + Project work Seminar	Dr. Sudarshan B, Mrs. Sahana Salagare

Time Table Co-ordinator

HOD

Principal



K.S. INSTITUTE OF TECHNOLOGY, BANGALORE -109
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
VII SEMESTER TIME TABLE FOR THE YEAR 2019 (ODD SEMESTER)

W.E.F. : 21.8.2019

CLASS TEACHER : Mr. SALEEM S TEVARAMANI

SEC : 'B'

CLASS ROOM : OB LH 311

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.30 PM	5	6	7	
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.30 PM 2.25 PM	2.25 PM 3.20 PM	3.20 PM 4.15 PM	
MON	← ADC LAB(15ECL76) - B1/VLSI LAB (15ECL77) -B3 →			MWA (15EC71)	L U N C H B R E A K	L U N C H B R E A K	CRYPT (15EC744)	SC (15EC755)	T	
TUE	CRYPT (15EC744)	DIP (15EC72)	T E A B R E A K	MWA (15EC71)			PE (15EC73)	← ADC LAB(15ECL76) - B2 / VLSI LAB(15ECL77) -B1 →		
WED	PE (15EC73)	CRYPT		SC (15EC755)			MWA (15EC71)	← ADC LAB(15ECL76)/ - B3 VLSI LAB (15ECL77) -B2 →		
THU	SC (15EC755)	DIP (15EC72)		CRYPT (15EC744)			PE (15EC73)	← PROJECT WORK PHASE - I (15ECP78) →		
FRI	CRYPT (15EC744)	PE (15EC73)		DIP (15EC72)			MWA (15EC71)	SC (15EC755)	DIP (15EC72)	T

Sub-Code	Subject Name	Faculty Name
15EC71	Microwave and Antennas	Dr. B Surekha
15EC72	Digital Image Processing	Mr. Saleem S Tevaramani
15EC73	Power Electronics	Mr. Christo Jain
15EC744	Cryptography (Professional Elective-3)	Dr. P.N Sudha
15EC755	Satellite Communication (Professional Elective-4)	Mrs.Sangeetha.V
15ECL76	Advanced Communication Lab	Mrs. Bhargavi Ananth, Mr. Sampath Kumar S, Mr. Saleem S Tevaramani
15ECL77	VLSI Lab	Mr. Praveen.A, Mrs. Shilpa, Mrs. Pooja S
15ECP78	Project Work Phase-I + Project work Seminar	Dr. Sudarshan B, Mrs. Sahana Salagare

Time Table Co-ordinator

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Principal

K.S. INSTITUTE OF TECHNOLOGY, BANGALORE -109
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
M.TECH . DIGITAL ELECTRONICS & COMMUNICATION TIME TABLE FOR THE YEAR 2019 (ODD semester)

W.E.F. :

CLASS TEACHER :

SEM : I

LECTURE HALL : 302 new building

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	2.10 PM 3.05 PM	3.05 PM 4.00 PM
MON			T E A B R E A K			L U N C H B R E A K			
TUE									
WED									
THU									
FRI									
SAT									

NO ADMISSIONS

Sub-Code	Subject Name	Faculty Name

Time Table Co-ordinator

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