



**K.S. INSTITUTE OF TECHNOLOGY, BANGALORE -109**  
**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**  
**III SEMESTER TIME TABLE FOR THE YEAR 2018 (ODD SEMESTER)**

W.E.F. : 30/7/2018

CLASS TEACHER : PRAGATHI . P

SEC : 'A'

CLASS ROOM : OB LH 201

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	1.15 PM 2.10 PM	2.10 PM 3.05 PM
MON	← AE(A3) / DE LAB (A1) →				EI	L U N C H B R E A K	AE	MATHS	KANNADA
TUE	MATHS	AE	T E A B R E A K	EE	DE		NA	EI	EE
WED	DE	NA		AE	EE		← AE(A1)/ DE LAB(A2) →		
THU	EE	NA		EI	MATHS		DE	AE	AE(ODD)/ DE(EVEN)
FRI	← AE(A2) / DE LAB (A3) →				EI		DE	MATHS	NA

Sub-Code	Subject Name	Faculty Name
17MAT31	Engineering Mathematics –III	Mrs. P. Jayashree
17EC32	Electronic Instrumentation	Mr.Deekshith .B.N
17EC33	Analog Electronics	Mr. B.R.Santhosh Kumar
17EC34	Digital Electronics	Mrs.Sangeetha.V
17EC35	Network Analysis	Mrs. R. Nithya Kumari
17EC36	Engineering Electromagnetics	Mrs. Pragati. P
17ECL37	Analog Electronics Lab	Mr. B.R.Santhosh Kumar(A1,A2,A3), Mrs. Bhargavi (A1,A3) Mrs. Sindhu(A1,A2,A3), Mr.Deekshith .B.N(A2)
17ECL38	Digital Electronics Lab	Mrs.Sangeetha.V(A1,A2,A3), Mr. Sampath Kumar. S,(A1,A3) Mrs. Shilpa (A2)
17KL/CPH39/49	Kannada/ Constitution of India Professional Ethics and Human Rights	

Time Table Co-ordinator

HOD

Principal



**K.S. INSTITUTE OF TECHNOLOGY, BANGALORE -109**  
**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**  
**III SEMESTER TIME TABLE FOR THE YEAR 2018 (ODD SEMESTER)**

W.E.F. : 30/7/2018

CLASS TEACHER : SAMPATH KUMAR . S

SEC : 'B'

CLASS ROOM : OB LH 203

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	2.10 PM 3.05 PM	2.10 PM 3.05 PM
MON	AE	EE	T E A	DE	NA(ODD)/ AE(EVEN)	L U N C H B R E A K	← AE(B1) / DE LAB (B3) →		
TUE	EI	NA		MATHS	DE		AE	EI	EE
WED	← AE(B2) / DE LAB(B1) →				EE		NA	MATHS	KANNADA
THU	NA	MATHS	B R E A K	AE	EI		← AE(B3) / DE LAB (B2) →		
FRI	MATHS	DE		EE	NA		DE	AE	EI

Sub-Code	Subject Name	Faculty Name
17MAT31	Engineering Mathematics –III	Mr.Naveen . V
17EC32	Electronic Instrumentation	Mr. Sampath Kumar. S
17EC33	Analog Electronics	Mrs. Pragati. P
17EC34	Digital Electronics	Mrs. Veda . B
17EC35	Network Analysis	Mr. Christo Jain
17EC36	Engineering Electromagnetics	Mrs. Jayasudha B.S.K
17ECL37	Analog Electronics Lab	Mrs. Pragati. P(B1,B2,B3) Mrs. Sahana Salagare, (B1,B2,B3)Mr.Deekshith (B1,B2).B.N, Mrs. Bhargavi(B3)
17ECL38	Digital Electronics Lab	Mrs. Veda . B(B1,B2,B3) , Mr. Sampath Kumar. S(B1,B3), Mrs. Priyadarshini J patil (B2)
17KL/CPH39/49	Kannada/ Constitution of India Professional Ethics and Human Rights	

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**K.S. INSTITUTE OF TECHNOLOGY, BANGALORE -109**  
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**V SEMESTER TIME TABLE FOR THE YEAR 2018 (ODD SEMESTER)**

W.E.F. : 30/7/2018

CLASS TEACHER : NITHYAKUMARI. R

SEC : 'A'

CLASS ROOM : OB LH 204

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	1.15 PM 2.10 PM	2.10 PM 3.05 PM
MON	ITC	DSP	T E A  B R E A K	HDL	OS	L U N C H  B R E A K	← DSP(A1)/ HDL LAB(A3) →		
TUE	DSP	HDL		M&E	C++/MC		ITC	OS	M&E
WED	C++/MC	OS		ITC	DSP		HDL	DSP(T)	T
THU	OS	M&E		C++/MC	ITC		← DSP(A2)/ HDL LAB(A1) →		
FRI	← DSP(A3)/ HDL LAB(A2) →			C++/MC	DSP		M&E	HDL	

Sub-Code	Subject Name	Faculty Name
15ES51	Management and Entrepreneurship Development	Mrs. Bhargavi
15EC52	Digital Signal Processing	Mrs. R. Nithya Kumari
15EC53	Verilog HDL	Dr. B.Sudharshan
15EC54	Information Theory & Coding	Mr. Sunil Kumar.G.R
15EC553	Operating System (Professional Elective-1)	Mrs. Lakshmi. H.R
15EC562	Object Oriented Programming Using C++ (Open Elective-1)	Dr P Joy Prabhakaran
15EC563	8051 Microcontroller (Open Elective-1)	Mr. B.R.Santhosh Kumar
15ECL57	DSP Lab	Mrs. R. Nithya Kumari, Mr. Christo Jain
15ECL58	HDL Lab	Dr. B.Sudharshan,Mr. Sunil Kumar.G.R

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**V SEMESTER TIME TABLE FOR THE YEAR 2018 (ODD SEMESTER)**

W.E.F. : 30/7/2018

CLASS TEACHER : PRAVEEN. A

SEC : 'B'

CLASS ROOM : OB LH 205

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	2.10 PM 3.05 PM	3.05 PM 4.00 PM
MON	OS	ITC	T E A	HDL	ITC	L U N C H  B R E A K	DSP	HDL	T
TUE	← DSP(B1)/ HDL LAB(B3) →			C++/MC			M&E	OS	ITC (T)
WED	C++/MC	DSP	B R E A K	OS	M&E		← DSP(B2)/ HDL LAB(B1) →		
THU	M&E	ITC		C++/MC	OS		DSP	ITC	HDL
FRI	DSP	HDL		M&E	C++/MC		← DSP(B3)/ HDL LAB(B2) →		

Sub-Code	Subject Name	Faculty Name
15ES51	Management and Entrepreneurship Development	Dr. Sangappa.S.B
15EC52	Digital Signal Processing	Mrs. Jayasudha B.S.K
15EC53	Verilog HDL	Mrs. Aruna Rao B.P
15EC54	Information Theory & Coding	Mr. Praveen.A
15EC553	Operating System (Professional Elective-1)	Dr. B.Sudharshan
15EC562	Object Oriented Programming Using C++ (Open Elective-1)	Dr. P Joy Prabhakaran
15EC563	8051 Microcontroller (Open Elective-1)	Mr. B.R.Santhosh Kumar
15ECL57	DSP Lab	Mrs. Jayasudha B.S.K, Mr.Deekshith .B.N
15ECL58	HDL Lab	Mrs. Aruna Rao B.P , Mr. Sunil Kumar.G.R

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**K.S. INSTITUTE OF TECHNOLOGY, BANGALORE -109**  
**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**  
**VII SEMESTER TIME TABLE FOR THE YEAR 2018 (ODD SEMESTER)**

W.E.F. : 6/8/2018

CLASS TEACHER : LAKSHMI. H .R

SEC : 'A'

CLASS ROOM : OB LH 310

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	2.10 PM 3.05 PM	3.05 PM 4.00 PM
MON	PE/DSDV	CRYP	T E A	DIP	SC	L U N C H  B R E A K	MWA	CRYP	T
TUE	← ADC(A1) / VLSI LAB(A3) →			MWA			SC	PE/DSDV	T
WED	MWA	DIP	B R E A K	MWA	PE/DSDV		← ADC(A2) / VLSI LAB(A1) →		
THU	DIP	SC		PE/DSDV	CRYP		← PROJECT / INTERNSHIP →		
FRI	MWA	DIP		CRYP	SC		← ADC(A3) / VLSI LAB(A2) →		

Sub-Code	Subject Name	Faculty Name
15EC71	Microwave and Antennas	Dr. P.N Sudha
15EC72	Digital Image Processing	Dr. B Surekha
15EC73	Power Electronics	Mr. Christo Jain
15EC744	Cryptography (Professional Elective-3)	Mrs. Lakshmi. H.R
15EC755	Satellite Communication (Professional Elective-4)	Mr. Saleem S Tevaramani
15ECL76	Advanced Communication Lab	Dr. P Joy Probhakaran , Mrs. Sahana Salagare
15ECL77	VLSI Lab	Mr. Praveen.A, Mrs. Lakshmi. H.R
15ECP78	Project Work Phase-I + Project work Seminar	All Staff
	Internship	All Staff
15EC663 6th Sem	Digital System Design Using Verilog (Open Elective - 2)	Mrs. Aruna Rao B.P

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**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**  
**VII SEMESTER TIME TABLE FOR THE YEAR 2018 (ODD SEMESTER)**

W.E.F. : 6/8/2018

CLASS TEACHER : SALEEM S TEVARAMANI

SEC : 'B'

CLASS ROOM : OB LH 311

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7	
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	2.10 PM 3.05 PM	3.05 PM 4.00 PM	
MON	PE/DSDV	CRYP	T E A B R	MWA	SC	L U N C H  B R E A K	← ADC(B1) / VLSI LAB(B3) →			
TUE	MWA	SC		CRYP	DIP		SC	PE/DSDV	T	
WED	← ADC(B2) / VLSI LAB(B1) →				PE/DSDV		CRYP	DIP	T	
THU	MWA	SC		E A K	PE/DSDV		DIP	MWA	CRYP	T
FRI	← ADC(B3) / VLSI LAB(B2) →				DIP		MWA	← PROJECT/INTERNSHIP →		

Sub-Code	Subject Name	Faculty Name
15EC71	Microwave and Antennas	Dr. P.N Sudha
15EC72	Digital Image Processing	Dr. B Surekha
15EC73	Power Electronics	Mrs. Veda . B
15EC744	Cryptography (Professional Elective-3)	Mr. Saleem S Tevaramani
15EC755	Satellite Communication (Professional Elective-4)	Mrs.Sangeetha.V
15ECL76	Advanced Communication Lab	Mr. Saleem S Tevaramani, Mrs. Bhargavi
15ECL77	VLSI Lab	Mr. Praveen.A, Mrs. Shilpa
15ECP78	Project Work Phase-I + Project work Seminar	All Staff
	Internship	All Staff
15EC663 6th Sem	Digital System Design Using Verilog (Open Elective - 2)	Mrs. Aruna Rao B.P

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**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**  
**VII SEMESTER TIME TABLE FOR THE YEAR 2018 (ODD SEMESTER)**

**Parallel classes for VII semester**

W.E.F. : 6/8/2018

CLASS TEACHER : LAKSHMI. H .R

SEC : 'A'

CLASS ROOM :OB LH 310

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	1.15 PM 2.10 PM	2.10 PM 3.05 PM
MON	PE	OFC	T E A  B R E A K	IP	OFC	L U N C H B R E A K	ESD	DSPA	
TUE	CCN	IP		ESD	OFC		ESD	PE	CCN
WED	DSPA	IP		DSPA	PE		← VLSI LAB →		
THU	IP	ESD		PE	CCN		CCN	T	T
FRI	CCN	IP		OFC	DSPA		← PE LAB →		

Sub-Code	Subject Name	Faculty Name
10EC71	Computer Communication Networks	Mr. Saleem S Tevaramani
10EC72	Optical Fiber Communication	Mrs. Sahana Salagare
10EC73	Power Electronics	Mr. Christo Jain
10EC74	Embedded System Design	Mr. B.R.Santhosh Kumar
10EC751	DSP Algorithms and Architecture (Elective -II (Group B)	Mrs. Jayasudha B.S.K
10EC763	Image Processing (Elective - III (Group C)	Dr. B Surekha
10ECL77	VLSI Lab	Mr. Praveen.A
10ECL78	Power Electronics Lab	Mr. Christo Jain

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**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**  
**M.TECH . DIGITAL ELECTRONICS & COMMUNICATION TIME TABLE FOR THE YEAR 2018 (ODD semester )**

W.E.F. :

CLASS TEACHER :

SEM : I

LECTURE HALL : 302 new building

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	2.10 PM 3.05 PM	3.05 PM 4.00 PM
MON			T E A  B R E A K			L U N C H  B R E A K			
TUE									
WED									
THU									
FRI									
SAT									

NO ADMISSIONS

Sub-Code	Subject Name	Faculty Name

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