

K.S. INSTITUTE OF TECHNOLOGY, BANGALORE -109
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
III SEMESTER TIME TABLE FOR THE YEAR 2017 (ODD SEMESTER)

W.E.F. : 7/08/2017

CLASS TEACHER : MRS. NITHYA KUMARI

SEC : 'A' SEC

LECTURE HALL : 201

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	2.10 PM 3.05 PM	3.05 PM 4.00 PM
MON	NA	AE	T E A B R E A K	MATHS	EE	L U N C H B R E A K	DE	NA	DE(T)
TUE	MATHS	EE		AE	DE		NA	AE	EI(T)
WED	EE	AE		EI	EE		← DE LAB (A1) / AEC LAB (A2) →		
THU	EI	EE		EI	NA		MATHS	DE	AE(T)
FRI	NA	MATHS		DE	EI		← DE LAB (A2) / AEC LAB (A1) →		

Sub-Code	Subject Name	Handled by Professor
15MAT31	Engineering Mathematics –III	Mrs. P. Jalaja Mr. Chowdappa
15EC32	Analog Electronics	Mrs. Pragati. P
15EC33	Digital Electronics	Mrs. Nithya Kumari
15EC34	Network Analysis	Dr. P.N Sudha
15EC35	Electronic Instrumentation	Mr. Sampath Kumar. S
15EC36	Engineering Electromagnetics	Dr. M.J Shanthi Prasad
15ECL37	Analog Electronics Lab	Mrs. Pragati.Mr. K.V.Madhava Murthy, Mrs. Aruna Rao B.P , Mrs. Veda
15ECL38	Digital Electronics Lab	Mrs. Nithya Kumari,Mrs. Prathima B. A, Mrs. Veda. B

Time Table Co-ordinator

HOD

Principal

K.S. INSTITUTE OF TECHNOLOGY, BANGALORE -109
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
III SEMESTER TIME TABLE FOR THE YEAR 2017 (ODD SEMESTER)

W.E.F. : 7/08/2017

CLASS TEACHER : MRS. PRAGATHI . P

SEC : 'B' SEC

LECTURE HALL : 203

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	1.15 PM 2.10 PM	2.10 PM 3.05 PM
MON	NA	AE	T E A B R E A K	MATHS	EI	L U N C H B R E A K	EE	DE	EE(T)
TUE	EE	NA		DE	AE		← DE LAB (B1) / AEC LAB (B2) →		
WED	AE	MATHS		NA	EE		DE	EI	DE(T)
THU	MATHS	EI		EE	NA		← DE LAB (B2) / AEC LAB (B1) →		
FRI	NA	DE		AE	EI		MATHS	AE	MATHS(T)

Sub-Code	Subject Name	Handled by Professor
15MAT31	Engineering Mathematics –III	Mr. P.Jayashree, Mr. B.S. Venkataramana
15EC32	Analog Electronics	Mr. B.R.Santhosh Kumar
15EC33	Digital Electronics	Mrs. Prathima B. A
15EC34	Network Analysis	Mrs. Nithya Kumari
15EC35	Electronic Instrumentation	Mrs. Priyadarshini .J. Patil
15EC36	Engineering Electromagnetics	Mrs. Pragati. P
15ECL37	Analog Electronics Lab	Mr. B.R.Santhosh Kumar, Mr. K.V.Madhava Murthy, Mrs. Veda
15ECL38	Digital Electronics Lab	Mrs. Prathima B., Mrs. Nithya Kumari, Mr. Saleem S Tevaramani

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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
III SEMESTER TIME TABLE FOR THE YEAR 2017 (ODD SEMESTER)

W.E.F. : 7/08/2017

CLASS TEACHER : MRS. SAHANA SALAGARE

SEC : 'C' SEC

LECTURE HALL : 007

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	2.10 PM 3.05 PM	3.05 PM 4.00 PM
MON	MATHS	NA	T E A B R E	EI	AE	L U N C H B R E A K	← DE LAB (C1) / AEC LAB (C2) →		
TUE	DE	EE		NA	DE		AE	EI	AE(T)
WED	EE	EI		DE	MATHS		EE	NA	NA(T)
THU	AE	NA		DE	EI		MATHS	DE	MATHS(T)
FRI	← DE LAB (C2) / AEC LAB (C1) →			MATHS	EE		AE	EE(T)	
SAT			A K						

Sub-Code	Subject Name	Handled by Professor
15MAT31	Engineering Mathematics -III	
15EC32	Analog Electronics	Mrs. Aruna Rao
15EC33	Digital Electronics	Mrs. Veda
15EC34	Network Analysis	Mr. Christo Jain
15EC35	Electronic Instrumentation	Mrs. Sahana Salagare
15EC36	Engineering Electromagnetics	Mr. Deekshith
15ECL37	Analog Electronics Lab	Mrs. Aruna Rao, Mrs. Pragati. P, Mr. B.R.Santhosh Kumar, Mr. Deekshith
15ECL38	Digital Electronics Lab	Mrs. Veda, Mr. Saleem S Tevaramani ,

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K.S. INSTITUTE OF TECHNOLOGY, BANGALORE -109
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
V SEMESTER TIME TABLE FOR THE YEAR 2017 (ODD SEMESTER)

W.E.F. : 7/08/2017

CLASS TEACHER : Mrs. JAYASUDHA B.S.K

SEC : 'A' SEC

LECTURE HALL : 204

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	1.15 PM 2.10 PM	2.10 PM 3.05 PM
MON	C++	DSP	T E A B R E A K	ITC	HDL	L U N C H B R E A K	← DSP LAB (A1) / HDL LAB (A3) →		
TUE	ITC	HDL		C++	OS		C++	M&E	DSP(T)
WED	HDL	OS		DSP	ITC		← DSP LAB (A2) / HDL LAB (A1) →		
THU	OS	M&E		ITC	HDL		M&E	DSP	ITC/C++
FRI	C++	M&E		DSP	OS		← DSP LAB (A3) / HDL LAB (A2) →		
SAT									

Sub-Code	Subject Name	Handled by Professor
15ES51	Management and Entrepreneurship Development	Mr. Sangappa.S.B
15EC52	Digital Signal Processing	Mrs. Jayasudha B.S.K
15EC53	Verilog HDL	Mr. Sunil Kumar.G.R
15EC54	Information Theory & Coding	Mr. Praveen.A
15EC553	Operating System (Professional Elective-1)	Dr. B.Sudharshan
15EC562	Object Oriented Programming Using C++ (Open Elective-1)	Mr. Sampath Kumar. S
15ECL57	DSP Lab	Mrs. Jayasudha B.S.K, Dr. B Surekha
15ECL58	HDL Lab	Mr. Sunil Kumar.G.R, Mr. Sampath Kumar. S

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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
V SEMESTER TIME TABLE FOR THE YEAR 2017 (ODD SEMESTER)

W.E.F. : 7/08/2017

CLASS TEACHER : Mrs. LAKSHMI. H.R

SEC : 'B' SEC

LECTURE HALL : 205

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	2.10 PM 3.05 PM	3.05 PM 4.00 PM
MON	DSP	C++	T E A B R E A K	HDL	DSP	L U N C H B R E A K	M&E	OS	ITC/HDL
TUE	ITC	OS		C++	DSP		← DSP LAB (B1) / HDL LAB (B3) →		
WED	C++	ITC		OS	M&E		HDL	M&E	DSP(T)
THU	ITC	DSP		HDL	C++		← DSP LAB (B2) / HDL LAB (B1) →		
FRI	← DSP LAB (B3) / HDL LAB (B2) →			ITC	OS		HDL	M&E	
SAT									

Sub-Code	Subject Name	Handled by Professor
15ES51	Management and Entrepreneurship Development	Mr. Sangappa.S.B
15EC52	Digital Signal Processing	Mrs.Sangeetha.V
15EC53	Verilog HDL	Dr. B.Sudharshan
15EC54	Information Theory & Coding	Dr. P.N Sudha
15EC553	Operating System (Professional Elective-1)	Mrs. Lakshmi. H.R
15EC562	Object Oriented Programming Using C++ (Open Elective-1)	Mrs. Aruna Rao
15ECL57	DSP Lab	Mrs.Sangeetha.V, Mr. Deekshith
15ECL58	HDL Lab	Dr. B.Sudharshan, Mr. Sunil Kumar.G.R

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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
V SEMESTER TIME TABLE FOR THE YEAR 2017 (ODD SEMESTER)

PARALLEL CLASSES FOR V SEMESTER

W.E.F. : 7/08/2017

CLASS TEACHER : Mrs. JAYASUDHA B.S.K

SEC : 'A' SEC

LECTURE HALL : 204

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	2.10 PM 3.05 PM	3.05 PM 4.00 PM
MON	AC	DSP	T E A B R E A K	ITC	MWR	L U N C H B R E A K	← DSP LAB (A1) /AC+LIC LAB (A3) →		
TUE	ITC	MWR		AC	VLSI		AC	M&E	DSP(T)
WED	MWR	VLSI		DSP	ITC		← DSP LAB (A2) /AC+LIC LAB (A3) →		
THU	VLSI	M&E		ITC	MWR		M&E	DSP	ITC/C++
FRI	AC	M&E		DSP	VLSI		← DSP LAB (A3) /AC+LIC LAB (A3) →		
SAT									

Sub-Code	Subject Name	Handled by Professor
10AL51	Management and Entrepreneurship	Mr. Sangappa.S.B
10EC52	Digital Signal Processing	Mrs. Jayasudha B.S.K
10EC53	Analog communication	Mrs. Sangeetha
10EC54	Microwaves and Radar	Mr. K.V. Madhavamurthy
10EC55	Information Theory & Coding	Mr. Praveen.A
10EC56	Fundamentals of CMOS VLSI	Dr Shanthi prasad
10ECL57	DSP Lab	Mrs. Jayasudha B.S.K, Dr. B Surekha
10ECL58	Analog Communication Lab + LIC lab	Dr. P.N. Sudha, Mr. Saleem S Tevaramani

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K.S. INSTITUTE OF TECHNOLOGY, BANGALORE -109
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
VII SEMESTER TIME TABLE FOR THE YEAR 2017 (ODD SEMESTER)

W.E.F. : 7/08/2017

CLASS TEACHER :MR. B.R.SANTHOSH KUMAR

SEC : 'A' SEC

LECTURE HALL : 310

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM	1.15 PM 2.10 PM	2.10 PM 3.05 PM
MON	CCN	PE	T E A B R E A K	IP	DSPA/AESD	L U N C H B R E A K	← PE LAB (A1) /VLSI LAB (A3) →		
TUE	OFC	ESD		PE	DSPA/AESD		IP	CCN	T
WED	DSPA/AESD	IP		ESD	OFC		← PE LAB (A2) /VLSI LAB (A1) →		
THU	DSPA/AESD	ESD		PE	IP		CCN	OFC	T
FRI	ESD	PE		OFC	CCN		← PE LAB (A3) /VLSI LAB (A2) →		
SAT									

Sub-Code	Subject Name	Handled by Professor
10EC71	Computer Communication Networks	Mr. Praveen.A
10EC72	Optical Fiber Communication	Mrs. Sahana Salagare
10EC73	Power Electronics	Mrs. Vishalini Divakar
10EC74	Embedded System Design	Mr. B.R.Santhosh Kumar
10EC75	DSP Algorithms and Architecture (Elective -II (Group B)	Dr. Surekha B
10EC75	Applied Embeded Systems Design(Elective -II (Group B)	Mr. Lakshmi H.R
10EC763	Image Processing (Elective - III (Group C)	Mr. Saleem S Tevaramani
10ECL77	VLSI Lab	Mr. Praveen.A, Dr. M.J Shanthi Prasad
10ECL78	Power Electronics Lab	Mrs. Vishalini Divakar, Mrs. Sahana Salagare

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VII SEMESTER TIME TABLE FOR THE YEAR 2017 (ODD SEMESTER)

W.E.F. : 7/08/2017

CLASS TEACHER : MR. SALEEM S TEVARAMANI

SEC : 'B' SEC

LECTURE HALL : 311

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	2.10 PM 3.05 PM	3.05 PM 4.00 PM
MON	CCN	IP	T E A C H E R B R E A K	ESD	DSPA/AESD	L U N C H B R E A K	PE	OFC	T
TUE	ESD	OFC		CCN	DSPA/AESD		← PE LAB(B1)/VLSI LAB (B3) →		
WED	DSPA/AESD	PE		ESD	IP		OFC	CCN	T
THU	DSPA/AESD	ESD		IP	PE		← PE LAB (B2)/VLSI LAB (B1) →		
FRI	← PE LAB (B3) /VLSI LAB (B2) →			IP	OFC		PE	CCN	
SAT			A K						

Sub-Code	Subject Name	Handled by Professor
10EC71	Computer Communication Networks	Mr. Saleem S Tevaramani
10EC72	Optical Fiber Communication	Mrs.Sangeetha.V
10EC73	Power Electronics	Mr. Christo Jain
10EC74	Embedded System Design	Mrs. Prathima. B.A
10EC75	DSP Algorithms and Architecture (Elective -II (Group B)	Mrs. Jayasudha B.S. K
10EC75	Applied Embeded Systems Design (Elective -II(Group B)	Mr. Lakshmi H.R
10EC763	Image Processing (Elective - III (Group C)	Dr. Surekha . B
10ECL77	VLSI Lab	Mr. M.J Shanthi Prasad, Mrs. Lakshmi. H.R
10ECL78	Power Electronics Lab	Mr. Christo Jain, Mrs. Priyadarshini .J. Patil,

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K.S. INSTITUTE OF TECHNOLOGY, BANGALORE -109
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

M.TECH . DIGITAL ELECTRONICS & COMMUNICATION TIME TABLE FOR THE YEAR 2017 (ODD SEMESTER)

W.E.F. :

CLASS TEACHER : MRS. SAHANA SALAGARE

SEM : I

LECTURE HALL : 302 NEW BUILDING

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	2.10 PM 3.05 PM	3.05 PM 4.00 PM
MON	ADC	ATD	T E A B R E A K	MATHS	OCN	L U N C H B R E A K			
TUE	ATD	ADC		MATHS	AES		← AC LAB -I →		
WED	AES	OCN		MATHS	ADC		← AC LAB -I →		
THU	OCN	AEC		ADC	ATD				
FRI	ATD	OCN		AES	MATHS				
SAT									

Sub-Code	Subject Name	Handled by Professor
16ELD11	Advanced Engineering Mathematics	Dr. Jayakumar
16ECS12	Antenna Theory and Design	Mrs. Sahana (Balakka) Salagare
16EVE13	Advanced Embedded System	Mrs. Prathima B.A
16ECS14	Advanced Digital Communications	R. Nithya Kumari
16ECS153	Optical Communication and Networking	Mrs. Lakshmi H R
16ECSL16	Advanced Communication Lab	Dr. P.N Sudha, Mrs. Nitya Kumari . R
16ECS17	Seminar on advanced topics from refereed journals	Dr. Shanthi Prasad.M .J ,Mr. Sunil Kumar G.R

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K.S. INSTITUTE OF TECHNOLOGY, BANGALORE -109
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
III SEMESTER TIME TABLE FOR THE YEAR 2017 (ODD SEMESTER)

W.E.F. : 7/08/2017

LAB : AE

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	2.10 PM 3.05 PM	3.05 PM 4.00 PM
MON			T E A B R E			L U N C H B R E A K	← AEC LAB (C2) →		
TUE							← AEC LAB (B2) →		
WED							← AEC LAB (A2) →		
THU							← AEC LAB (B1) →		
FRI	← AEC LAB (C1) →						← AEC LAB (A1) →		
SAT			A K						

Sub-Code	Subject Name	Handled by Professor
15ECL37	Analog Electronics Lab	Mrs. Pragati.Mr. K.V.Madhava Murthy, Mrs. Aruna Rao B.P , Mrs. Veda
15ECL37	Analog Electronics Lab	Mr. B.R.Santhosh Kumar, Mr. K.V.Madhava Murthy,Mrs. Veda
15ECL37	Analog Electronics Lab	Mrs. Aruna Rao,Mrs. Pragati. P,Mr. B.R.Santhosh Kumar,Mr. Deekshith

Time Table Co-ordinator

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K.S. INSTITUTE OF TECHNOLOGY, BANGALORE -109
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
III SEMESTER TIME TABLE FOR THE YEAR 2017 (ODD SEMESTER)

W.E.F. : 7/08/2017

LAB : DE

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	2.10 PM 3.05 PM	3.05 PM 4.00 PM
MON			T E A B R E			L U N C H B R E A K	← DE LAB (C1) →		
TUE							← DE LAB (B1) →		
WED							← DE LAB (A1) →		
THU							← DE LAB (B2) →		
FRI	← DE LAB (C2) →				← DE LAB (A2) →				
SAT			A K						

Sub-Code	Subject Name	Handled by Professor
15ECL38	Digital Electronics Lab	Mrs. Prathima B. A, Mrs. Nithya Kumari, Mrs. Veda. B
15ECL38	Digital Electronics Lab	Mrs. Nithya Kumari, Mrs. Prathima B., Mr. Saleem S Tevaramani
15ECL38	Digital Electronics Lab	Mr. Saleem S Tevaramani, Mrs. Veda

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K.S. INSTITUTE OF TECHNOLOGY, BANGALORE -109
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
V SEMESTER TIME TABLE FOR THE YEAR 2017 (ODD SEMESTER)

W.E.F. : 7/08/2017

LAB : DSP

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	2.10 PM 3.05 PM	3.05 PM 4.00 PM
MON			T E A B R E A K			L U N C H B R E A K	← DSP LAB (A1) →		
TUE							← DSP LAB (B1) →		
WED							← DSP LAB (A2) →		
THU							← DSP LAB (B2) →		
FRI	← DSP LAB (B3) →						← DSP LAB (A3) →		
SAT									

Sub-Code	Subject Name	Handled by Professor
15ECL57	DSP Lab	Mrs. Jayasudha B.S.K, Dr. B Surekha
15ECL57	DSP Lab	Mrs.Sangeetha.V, Mr. Deekshith

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V SEMESTER TIME TABLE FOR THE YEAR 2017 (ODD SEMESTER)

W.E.F. : 7/08/2017

LAB : HDL

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	2.10 PM 3.05 PM	3.05 PM 4.00 PM
MON			T E A B R E A K			L U N C H B R E A K	← HDL LAB (A3) →		
TUE							← HDL LAB (B3) →		
WED							← HDL LAB (A1) →		
THU							← HDL LAB (B1) →		
FRI	← HDL LAB (B2) →						← HDL LAB (A2) →		
SAT									

Sub-Code	Subject Name	Handled by Professor
15ECL58	HDL Lab	Mr. Sunil Kumar.G.R, Mr. Sampath Kumar. S
15ECL58	HDL Lab	Mr. Sunil Kumar.G.R, Dr. B.Sudharshan

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VII SEMESTER TIME TABLE FOR THE YEAR 2017 (ODD SEMESTER)

W.E.F. : 7/08/2017

LAB : VLSI

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	2.10 PM 3.05 PM	3.05 PM 4.00 PM
MON			T E A B R E			L U N C H B R E A K	← VLSI LAB (A3) →		
TUE							← VLSI LAB (B3) →		
WED							← VLSI LAB (A1) →		
THU							← VLSI LAB (B1) →		
FRI	← VLSI LAB (B2) →				← VLSI LAB (A2) →				
SAT			A K						

Sub-Code	Subject Name	Handled by Professor
10ECL77	VLSI Lab	Mr. Praveen.A, Dr. M.J Shanthi Prasad
10ECL77	VLSI Lab	Mr. M.J Shanthi Prasad, Mrs. Lakshmi. H.R

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VII SEMESTER TIME TABLE FOR THE YEAR 2017 (ODD SEMESTER)

W.E.F. : 7/08/2017

LAB : PE

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	2.10 PM 3.05 PM	3.05 PM 4.00 PM
MON			T E A B R E A K			L U N C H B R E A K	← PE LAB (A1) →		
TUE							← PE LAB(B1) →		
WED							← PE LAB (A2) →		
THU							← PE LAB (B2) →		
FRI	← PE LAB (B3) →				← PE LAB (A3) →				
SAT									

Sub-Code	Subject Name	Handled by Professor
10ECL78	Power Electronics Lab	Mrs. Vishalini Divakar, Mrs. Sahana Salagare
10ECL78	Power Electronics Lab	Mr. Christo Jain, Mrs. Priyadarshini .J. Patil,

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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
M.TECH . DIGITAL ELECTRONICS & COMMUNICATION TIME TABLE FOR THE YEAR 2017 (ODD SEMESTER)

W.E.F.

CLASS TEACHER :

SEM : I

LECTURE HALL : 302

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	2.10 PM 3.05 PM	3.05 PM 4.00 PM
MON			T E A B R E A K			L U N C H B R E A K			
TUE									
WED									
THU									
FRI									
SAT									

Sub-Code	Subject Name	Handled by Professor
16ELD11	Advanced Engineering Mathematics	
16ECS12	Antenna Theory and Design	
16EVE13	Advanced Embedded System	
16ECS14	Advanced Digital Communications	
16EXX15X	Elective-1	
16ECSL16	Advanced Communication Lab	
16ECS17	Seminar on advanced topics from refereed journals	

Time Table Co-ordinator

HOD

Principal