




K.S. INSTITUTE OF TECHNOLOGY, BANGALORE -109
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
FDP JULY 2014- TO JUNE2015

SL. No.	Name of the Event	Date	Association Details	Resource person	Sem and Year	No Of Participants	Objectives	PO Mapped to	Photo
1	17th ISTE State Level Faculty Conventon	20th Nov 2014	Mr.Sangappa.B	Dr.Shivashankar,Principal, RNSIT, B'lore	ISTE students and faculty members	50	To upgrade the teaching and profetional skills of faculties	PO1,PO2,PO3,PO4,P06,P09	
2	Five Days FDP on VLSI Design using Cadence Tools	29th June 2015 to 3rd July 2015	Dr. M.J. Shanthi Prasad,Mrs.Supriya V.G,Mrs.Anitha Senthilkumar,Mrs Prathima B.A,Mrs Sahana Salagare,Mrs.Laxmi H.R	Dr.Jayagouri,BMS college,B'lore	Faculties of KSIT and other collages KSIT other	58	To enrich the knowledge of VLSI Design using cadence tools to research scholars, faculty members and post graduate students	PO6,PO7,PO9,PO10 ,PO11, PO12	