



K.S. INSTITUTE OF TECHNOLOGY, BANGALORE -109
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
III SEMESTER TIME TABLE FOR THE YEAR 2024 (ODD SEMESTER)

W.E.F. : 19/8/2024
 SEC : 'A'

CLASS TEACHER : Dr. Anita. P
 CLASS ROOM : OB LH 310

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	2.10 PM 3.05 PM	2.10 PM 3.05 PM
MON	NA (BEC304)	EPC (BEC303)	T E A B R E A K	DSDV (BEC302)	COA (BEC306C)	L U N C H B R E A K	← A& DSD Lab (BECL305) - A1 DSDV LAB (BEC302) - A2 EPC LAB (BEC303) - A3 →		
TUE	Maths (BMATEC301)	NA (BEC304)		COA (BEC306C)	DSDV (BEC302)		EPC (BEC303)	NA (BEC304)	SC& R (BSCK307)
WED	NA (BEC304)	EPC (BEC303)		Maths (BMATEC301)	COA (BEC306C)		← A& DSD Lab (BECL305) -A2 DSDV LAB (BEC302) - A3 EPC LAB (BEC303) - A1 →		
THU	NA (BEC304)	Maths (BMATEC301)		EPC (BEC303)	DSDV (BEC302)		← LABVIEW Programming LAB (BEC358A) - A1 (IOT LAB), -A2 (VLSI LAB) →		
FRI	← A& DSD Lab (BECL305) -A3 DSDV LAB (BEC302) -A1 EPC LAB (BEC303) - A2 →			Maths (BMATEC301)	DSDV (BEC302)		COA (BEC306C)	NSS (BNSK359) /SPORTS BPEK359/ Yoga (BYOK359)	

Sub-Code	Subject Name	Faculty Name
BMATEC301	AV Mathematics-III for EC Engineering	Mr. Purushotham P
BEC302	Digital System Design Using Verilog	Mrs. Kavya B M
BEC303	Electronic Principles and Circuits	Dr. Devika B
BEC304	Network Analysis	Dr. P.N Sudha
BECL305	Analog and Digital Systems Design Lab	Dr. Dinesh Kumar D S , Mrs. Vishalini Divakar
BEC306C	Computer Organization and Architecture (Elective)	Dr. Anita. P
BSCK307	Social Connect and Responsibility	Dr. Sangappa.S.B
BEC358A	LABVIEW Programming (Ability Enhancement Course/Skill Enhancement Course - III)	Mrs. Bhargavi Ananth , Dr. Saleem S Tevaramani, Mrs. Pragathi P, Mr. Naveen Kumar S.
BEC302	Digital System Design Using Verilog [Theory Lab]	Mrs. Kavya B M , Mr. B.R.Santhosh Kumar
BEC303	Electronic Principles and Circuits [Theory Lab]	Dr. Devika B, Mrs. Priyadharshini V
BNSK359	National Service Scheme (NSS)	Mr. Naveen
BPEK359	Physical Education (PE) (Sports and Athletics)	Mr. Umesh
BYOK359	Yoga	Mrs. Preethi K

[Signature]
 Time Table Co-ordinator

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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
III SEMESTER TIME TABLE FOR THE YEAR 2024 (ODD SEMESTER)

W.E.F. : 19/8/2024
 SEC : 'B'

CLASS TEACHER : Mrs. Priyadharshini . V
 CLASS ROOM : OB LH 311

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	2.10 PM 3.05 PM	3.05 PM 4.00 PM
MON	DSDV (BEC302)	NA (BEC304)	T E A B R E A K	Maths (BMATEC301)	COA (BEC306C)	L U N C H B R E A K	← LABVIEW Programming LAB (BEC358A) - B1 (IOT LAB), -B2 (VLSI LAB) →		
TUE	NA (BEC304)	EPC (BEC303)		COA (BEC306C)	DSDV (BEC302)		← A& DSD Lab (BECL305) -B1 DSDV LAB (BEC302) -B2 EPC LAB (BEC303)- B3 →		
WED	← A& DSD Lab (BECL305) -B2 DSDV LAB (BEC302) - B3 EPC LAB (BEC303) - B1 →			EPC (BEC303)	Maths (BMATEC301)		NA (BEC304)	SC& R (BSCK307)	
THU	EPC (BEC303)	COA (BEC306C)		DSDV (BEC302)	Maths (BMATEC301)		← A& DSD Lab (BECL305) -B3 DSDV LAB (BEC302) - B1 EPC LAB (BEC303) -B2 →		
FRI	NA (BEC304)	Maths (BMATEC301)		NA (BEC304)	COA (BEC306C)		EPC (BEC303)	DSDV (BEC302)	NSS (BNSK359) /SPORTS BPEK359/ Yoga (BYOK359)

Sub-Code	Subject Name	Faculty Name
BMATEC301	AV Mathematics-III for EC Engineering	Mr. Purushotham P
BEC302	Digital System Design Using Verilog	Mr. B.R.Santhosh Kumar
BEC303	Electronic Principles and Circuits	Dr. Devika B
BEC304	Network Analysis	Dr. P.N Sudha
BECL305	Analog and Digital Systems Design Lab	Mrs. Ramya K R., Mrs. Sapna Patil
BEC306C	Computer Organization and Architecture (Engineering Science Course Elective)	Mrs. Priyadharshini . V
BSCK307	Social Connect and Responsibility	Dr. Sangappa.S.B
BEC358A	LABVIEW Programming (Ability Enhancement Course/Skill Enhancement Course - III)	Mrs. Bhargavi Ananth , Dr. Saleem S Tevaramani, Mrs. Pragathi P, Mr. Naveen Kumar S.
BEC302	Digital System Design Using Verilog [Theory Lab]	Mrs. Kavya B M , Mr. B.R.Santhosh Kumar
BEC303	Electronic Principles and Circuits [Theory Lab]	Mrs. Priyadharshini V, Mr. Satish Kumar B
BNSK359	National Service Scheme (NSS)	Mr. Naveen
BPEK359	Physical Education (PE) (Sports and Athletics)	Mr. Umesh
BYOK359	Yoga	Mrs. Preethi K

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K.S. INSTITUTE OF TECHNOLOGY, BANGALORE -109
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
V SEMESTER TIME TABLE FOR THE YEAR 2024 (ODD SEMESTER)

W.E.F. : 9/9/2024
 SEC : 'A'

CLASS TEACHER : Mrs. Bhanumathi A
 CLASS ROOM : OB LH 305

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	1.15 PM 2.10 PM	2.10 PM 3.05 PM
MON	DSP (BEC502)	TIME (BEC501)	T E A B R E A K	DS (BEC515C)	DC (BEC503)	L U N C H B R E A K	← DC LAB (BECL504) - A1 DSP LAB (BEC502) - A2 →		
TUE	DC (BEC503)	DS (BEC515C)		DSP (BEC502)	RM (BRMK557)		TIME (BEC501)	EVS (BESK508)	EVS (BESK508)
WED	RM (BRMK557)	DSP (BEC502)		TIME (BEC501)	DS (BEC515C)		← DC LAB (BECL504) -A2 DSP LAB (BEC502) - A3 →		
THU	DS (BEC515C)	RM (BRMK557)		TIME (BEC501)	DC (BEC503)		← MINI PROJECT (BEC586) →		
FRI	← DC LAB (BECL504) - A3 DSP LAB (BEC502) -A1 →			RM (BRMK557)	DC (BEC503)		DSP (BEC502)	NSS (BNSK559) / PE (BPEK559)/ YOGA (BYOK559)	

Sub-Code	Subject Name	Faculty Name
BEC501	Technological Innovation and Management Enterprenership	Mrs. Vishalini Divakar
BEC502	Digital Signal Processing	Mrs. Bhanumathi A
BEC503	Digital Communication	Mrs.Sangeetha.V,
BECL504	Digital Communication Lab	Dr. Rekha N , Mrs. Suma Santhosh, Mrs. Pragathi P
BEC515C	Data Structure Using C++ (Professional Elective Course)	Mrs. Bhargavi Ananth
BEC586	Mini Project	Mrs. Bhargavi Ananth, Mrs. Vishalini Divakar
BRMK557	Research Methodology and IPR	Dr. Rekha N
BESK508	Envirmental Studies	
BNSK559	National Service Scheme (NSS)	Mr. Naveen
BPEK559	Physical Education (PE) (Sports and Athletics)	Mr. Umesh
BYOK559	Yoga	Mrs. Preethi K
BEC502	Digital Signal Processing [Theory Lab]	Dr. Electa Alice Jayarani A , Dr. Bharathi Gururaj , Mrs. Bhanumathi A

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V SEMESTER TIME TABLE FOR THE YEAR 2024 (ODD SEMESTER)

W.E.F. : 9/9/2024
 SEC : 'B'

CLASS TEACHER : Dr. Rekha N
 CLASS ROOM : OB LH 306

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	2.10 PM 3.05 PM	3.05 PM 4.00 PM
MON	DC (BEC503)	RM (BRMK557)	T E A	DSP (BEC502)	TIME (BEC501)	L U N C H B R E A K	← MINI PROJECT (BEC586) →		
TUE	DSP (BEC502)	RM (BRMK557)		TIME (BEC501)	DS (BEC515C)		← DC LAB (BECL504) - B1 DSP LAB (BEC502) - B2 →		
WED	← DC LAB (BECL504) - B2 DSP LAB (BEC502) - B3 →			RM (BRMK557)	DC (BEC503)		DS (BEC515C)	EVS (BESK508)	
THU	TIME (BEC501)	DC (BEC503)	B R E A K	DSP (BEC502)	DS (BEC515C)		← DC LAB (BECL504) - B3 DSP LAB (BEC502) - B1 →		
FRI	DS (BEC515C)	DC (BEC503)		EVS (BESK508)	DSP (BEC502)		TIME (BEC501)	RM (BRMK557)	NSS (BNSK559) / PE (BPEK559)/ YOGA (BYOK559)

Sub-Code	Subject Name	Faculty Name
BEC501	Technological Innovation and Management Enterprenership	Mrs. Vishalini Divakar
BEC502	Digital Signal Processing	Mrs. Bhanumathi A
BEC503	Digital Communication	Mrs.Sangeetha.V,
BECL504	Digital Communication Lab	Mrs.Sangeetha.V, Mrs. Suma Santhosh, Mrs. Pragathi P
BEC515C	Data Structure Using C++ (Professional Elective Course)	Mrs. Bhargavi Ananth
BEC586	Mini Project	Mrs. Bhargavi Ananth, Mrs. Vishalini Divakar
BRMK557	Research Methodology and IPR	Dr. Rekha N
BESK508	Envirmental Studies	
BNSK559	National Service Scheme (NSS)	Mr. Naveen
BPEK559	Physical Education (PE) (Sports and Athletics)	Mr. Umesh
BYOK559	Yoga	Mrs. Preethi K
BEC502	Digital Signal Processing [Theory Lab]	Dr. Electa Alice Jayarani A , Dr. Bharathi Gururaj , Mrs. Bhanumathi A

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VII SEMESTER TIME TABLE FOR THE YEAR 2024 (ODD SEMESTER)

W.E.F. : 9/9/2024
 SEC : 'A'

CLASS TEACHER : Dr. Saleem S Tevaramani
 CLASS ROOM : NB LH 302

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	2.10 PM 3.05 PM	3.05 PM 4.00 PM
MON	AI&ML (21CS752) / DS (21CS754)	O&WC (21EC72)	T E A C H E R A K	NS (21EC732)	AVLSI (21EC71)	L U N C H B R E A K	← Research Internship/ Industry Internship (21INT82) →		
TUE	DIP (21EC722)	AVLSI (21EC71)		O&WC (21EC72)	AI&ML (21CS752) / DS (21CS754)		← Research Internship/ Industry Internship (21INT82) →		
WED	O&WC (21EC72)	AVLSI (21EC71)		NS (21EC732)	DIP (21EC722)		← Research Internship/ Industry Internship (21INT82) →		
THU	AI&ML (21CS752) / DS (21CS754)	NS (21EC732)		DIP (21EC722)	O&WC (21EC72)		← Research Internship/ Industry Internship (21INT82) →		
FRI	DIP (21EC722)	NS (21EC732)		AVLSI (21EC71)	AI&ML (21CS752) / DS (21CS754)		← Research Internship/ Industry Internship (21INT82) →		

Sub-Code	Subject Name	Faculty Name
21EC71	Advanced VLSI	Mrs. Shubha Kulkarni
21EC72	Optical & Wireless Communication	Dr. Dinesh Kumar D S
21EC732	Digital Image Processing (Professional elective Course-II)	Dr. Saleem S Tevaramani
21EC742	Network Security (Professional elective Course-III)	Mr. Naveen Kumar S
21CS752	Introduction to AI and ML (Open elective Course-II)	Mrs. Pragathi P
21CS754	Introduction to Data Science (Open elective Course-II)	Dr. Electa Alice Jayarani A
21EC76	Project work	Dr. Devika B , Dr. Bharathi Gururaj

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VII SEMESTER TIME TABLE FOR THE YEAR 2024 (ODD SEMESTER)

W.E.F. : 9/9/2024
 SEC : 'B'

CLASS TEACHER : Dr. Dinesh Kumar D S
 CLASS ROOM : NB SH 303

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	2.10 PM 3.05 PM	3.05 PM 4.00 PM
MON	AI&ML (21CS752) / DS (21CS754)	AVLSI (21EC71)	T E A C H E R A K	DIP (21EC722)	O&WC (21EC72)	L U N C H B R E A K	← Research Internship/ Industry Internship (21INT82) →		
TUE	O&WC (21EC72)	NS (21EC732)		DIP (21EC722)	AI&ML (21CS752) / DS (21CS754)		← Research Internship/ Industry Internship (21INT82) →		
WED	NS (21EC732)	DIP (21EC722)		O&WC (21EC72)	AVLSI (21EC71)		← Research Internship/ Industry Internship (21INT82) →		
THU	AI&ML (21CS752) / DS (21CS754)	O&WC (21EC72)		AVLSI (21EC71)	NS (21EC732)		← Research Internship/ Industry Internship (21INT82) →		
FRI	AVLSI (21EC71)	DIP (21EC722)		NS (21EC732)	AI&ML (21CS752) / DS (21CS754)		← Research Internship/ Industry Internship (21INT82) →		

Sub-Code	Subject Name	Faculty Name
21EC71	Advanced VLSI	Mrs. Shubha Kulkarni
21EC72	Optical & Wireless Communication	Dr. Dinesh Kumar D S
21EC732	Digital Image Processing (Professional elective Course-II)	Dr. Saleem S Tevaramani
21EC742	Network Security (Professional elective Course-III)	Mr. Naveen Kumar S
21CS752	Introduction to AI and ML (Open elective Course-II)	Mrs. Pragathi P
21CS754	Introduction to Data Science (Open elective Course-II)	Dr. Electa Alice Jayarani A
21EC76	Project work	Dr. Devika B , Dr. Bharathi Gururaj

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