5.2 Student Progression
5.2.2 Student Progression to higher education in percentage during the year 2019-20

Department Computer Science and Engineering

Name of the Institution: BITS Pilani
Name of the Program: M.S (Work Integrated Learning Programmes)
USN: 1KS16CS008
Name: Anand TR

Work Integrated Learning Programmes **BITS Pilani**
Pilani | Dubai | Goa | Hyderabad

August 08, 2020

Ref: Adm/First Semester 2020-21/SP93/ASP9361986

Dear ANAND T R,

Congratulations!

With reference to your Application, we would like to inform you that you have been offered **provisional admission to the M.Tech. Software Engineering 4 Sem SAP degree programme** conducted by our Institute in collaboration with your employing organization.

You are yet to submit your provisional degree and all required marksheets. Please submit the same in Application Centre by 30th November 2020, failing which you will forfeit this admission offer.

You also understand that after you submit your provisional degree and all marksheets the admission cell of BITS Pilani will reassess your eligibility for the programme. If you fail to meet the eligibility criteria your admission will stand cancelled.

This unique method of education demands simultaneous obligation to meet the high academic standards of this Institute with the work demands of your sponsoring employer. This being a collaborative programme, it is mandatory that you remain an employee of your sponsoring organization till you complete the programme. You will cease to be a student of this programme if you cease to be an employee of your sponsoring organization. The requisite processing fee for your application is Rs.1,500.00/-. The fee schedule for this programme is: One time Admission fees of Rs. 16,500/-; Semester fees of Rs. 55,000/- for every semester of Registration. It may be noted that, in case you decide to discontinue from the programme after confirmation of Admission and Registration, you will not be entitled to seek refund of any fees paid.

Best Wishes,

Dean, WILP

To be filled and duly signed by the applicant without detaching the above part of this letter.

I ANAND T R herewith accept the admission offer as per above stated conditions and also confirm my Registration for the First Semester of my degree offered in First Semester 2020-21 of BITS calendar for the four courses as given below:

Course No.	Course Title	Units
SESAPZG512	Object Oriented Analysis & Design	4
SESAPZG518	Database Design and Application	5
SESAPZG519	Data Structures & Algorithms Design	5
SESAPZG622	Software Project Management	4

Department Electronics and Communication Engineering

Name of the Institution: Vellore Institute of Technology
Name of the Program: M.Tech in Embedded Systems
USN: 1KS16EC081
Name: Sai Tarun Tadiparti

VIT
Vellore Institute of Technology

PROVISIONAL ADMISSION LETTER

13-07-2020

SAI TARUN T, [Application No. 2020701092] has been provisionally admitted to **M.Tech. Embedded Systems** Programme at **Vellore** campus, for the academic year 2020-2021 based on merit performance and selection criteria. The orientation programme and commencement of classes through online mode will be on 27-July-2020 tentatively.

S.No.	STATEMENT OF APPROXIMATE EXPENDITURE in INR (For M.Tech. 2 Year)			
	Academic Fees	2020-21	2021-22	Total
1.	Tuition Fees (per annum)	190,000	190,000	380,000
2.	*Caution Deposit (refundable)	3,000	-	3,000
A. Academic Fees Total		193,000	190,000	383,000
Approximate Hostel Fees (if applicable)				
3.	Hostel Establishment Charges & Mess Charges	109,300	109,300	218,600
4.	Hostel Caution Deposit (refundable)	15,000	-	15,000
B. Estimated Hostel Fees (4-Bed AC, Non-Veg Food)		124,300	109,300	233,600
Grand Total [A+B]		317,300	299,300	616,600
Rupees Six Lakh Sixteen Thousand Six Hundred Only				

1. Please pay your tuition fee advance of Rs.1,00,000/- on or before 20-July-2020 and the remaining balance fee to be paid on or before 26-July-2020. The payment should be done through online mode only (Refer VIT website for online payment details). Non-payment within the deadline will automatically cancel the admission offer.

2. The details and procedure for hostel accommodation will be updated in the VIT Vellore/Chennai/AP/Bhopal websites shortly.

3. Hostel fee is applicable for those who join the hostel. Actuals might vary based on the choice of room and food in the respective campus.

4. If the candidate withdraws from the course before commencement of programme, Rs. 11,800/- (including GST) will be deducted. In case of withdrawal from hostel, refund rules will be announced shortly in VIT website.

5. Score in 10th, 12th class and UG degree need to be 60% and above. This is mandatory for VIT Campus Placement.

6. Change of campus and change of programme is not possible.

The sanction of **EDUCATION LOAN** to the student is recommended subject to the conditions of the lending bank.

Director (PG Admissions)
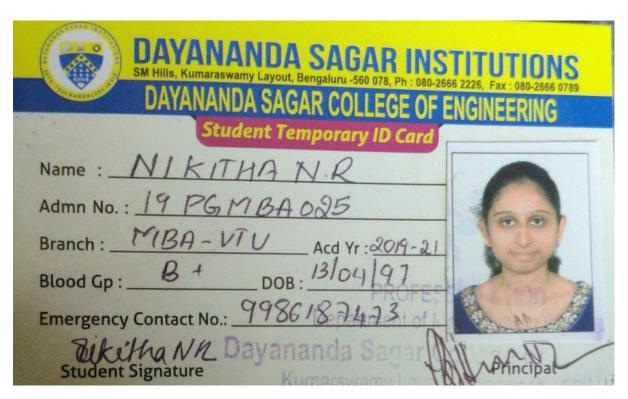Dr. V.Ramasubramanian

Agreed and counter signed by

Signature of the Candidate with date

Signature of the Parent / Guardian with date

Department Telecommunication and Engineering

Name of the Institution: Vellore Institute of Technology
Name of the Program: M.Tech in Embedded Systems
USN: 1KS15TE024
Name: Nikitha NR

ME :
NAME: KISHAN M
USN : 1KS15ME037
UNIVESITY: RAMAIAH UNIVERSITY