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INTERNSHIP CERTIFICATE

This is to certify that **Ms. Aadhya B N** bearing USN **1KS21EC001** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

Internship Coordinator

Dr P N Sudha
Prof.& Head ECE Dept



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INTERNSHIP CERTIFICATE

This is to certify that **Mr. Abhijith R** bearing USN **1KS21EC002** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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INTERNSHIP CERTIFICATE

This is to certify that **Mr. Abhishek H C** bearing USN **1KS21EC003** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Abhishek T S** bearing USN **1KS21EC004** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Aishwarya A S** bearing USN **1KS21EC005** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Akshay C** bearing USN **1KS21EC006** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Akshay M S** bearing USN **1KS21EC007** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Anagha Prakash** bearing USN **1KS21EC008** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Anirudha R Bhat** bearing USN **1KS21EC009** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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INTERNSHIP CERTIFICATE

This is to certify that **Ms. Archana G M** bearing USN **1KS21EC010** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Archana M** bearing USN **1KS21EC011** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Ashcharya N B** bearing USN **1KS21EC013** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Ashwin S R** bearing USN **1KS21EC014** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. B N Jeevan** bearing USN **1KS21EC015** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. B P Samarth** bearing USN **1KS21EC016** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. B S Bhargav** bearing USN **1KS21EC017** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Bhavya K** bearing USN **1KS21EC018** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Bhuvana H** bearing USN **1KS21EC019** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Bindushree S** bearing USN **1KS21EC020** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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INTERNSHIP CERTIFICATE

This is to certify that **Mr. Chintan D S** bearing USN **1KS21EC021** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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INTERNSHIP CERTIFICATE

This is to certify that **Mr. Chiranth V V** bearing USN **1KS21EC023** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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INTERNSHIP CERTIFICATE

This is to certify that **Ms. Daggupati Charitha** bearing USN **1KS21EC024** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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INTERNSHIP CERTIFICATE

This is to certify that **Ms. Damini S** bearing USN **1KS21EC025** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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INTERNSHIP CERTIFICATE

This is to certify that **Ms. Deeksha H K** bearing USN **1KS21EC026** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Deepika D** bearing USN **1KS21EC027** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Gagan V** bearing USN **1KS21EC028** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Gagana Sindhu N** bearing USN **1KS21EC029** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Greeshma S** bearing USN **1KS21EC030** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Gurushankara M** bearing USN **1KS21EC031** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Harini L** bearing USN **1KS21EC032** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Hemanth D R** bearing USN **1KS21EC033** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Kambhampati Vivek** bearing USN **1KS21EC035** has successfully completed the Internship on ‘**Simulation of Analog & Digital Circuits using Multisim**’ organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Karan S** bearing USN **1KS21EC036** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Keerthana S** bearing USN **1KS21EC037** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Komala N** bearing USN **1KS21EC038** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Kushal Gowda U** bearing USN **1KS21EC039** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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INTERNSHIP CERTIFICATE

This is to certify that **Ms. Kusuma M S** bearing USN **1KS21EC040** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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INTERNSHIP CERTIFICATE

This is to certify that **Ms. Likitha L** bearing USN **1KS21EC041** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Lohit S Hoolageri** bearing USN **1KS21EC042** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Lohith B** bearing USN **1KS21EC043** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Lohith S** bearing USN **1KS21EC044** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Manoj T V** bearing USN **1KS21EC045** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Meghana N** bearing USN **1KS21EC046** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Misba M** bearing USN **1KS21EC047** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Mithun C** bearing USN **1KS21EC048** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Monisha D** bearing USN **1KS21EC049** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Mutthuluru Sai Himaja** bearing USN **1KS21EC050** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Nandan K** bearing USN **1KS21EC051** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Narahari N Joshi** bearing USN **1KS21EC053** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Naveen S** bearing USN **1KS21EC054** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Nayana J** bearing USN **1KS21EC055** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Omkar N Bhujarkar** bearing USN **1KS21EC058** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Pavan M Pai** bearing USN **1KS21EC059** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Poluru Manjunath** bearing USN **1KS21EC060** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Pooja R** bearing USN **1KS21EC061** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Prajwal D** bearing USN **1KS21EC062** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Prajwal G V** bearing USN **1KS21EC063** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Prajwal H S** bearing USN **1KS21EC064** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Prajwal R** bearing USN **1KS21EC065** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Pratham R Shanbhag** bearing USN **1KS21EC066** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Prayag Singh S** bearing USN **1KS21EC067** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Preetham M** bearing USN **1KS21EC068** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Preksha S** bearing USN **1KS21EC069** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Punith M** bearing USN **1KS21EC070** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Raghavendra Narayan Pujar** bearing USN **1KS21EC071** has successfully completed the Internship on 'Simulation of Analog & Digital Circuits using Multisim' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Rakshith S** bearing USN **1KS21EC072** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Rakshitha M R** bearing USN **1KS21EC073** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Rayadurg Joish Shriya** bearing USN **1KS21EC074** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Rehaman Shariff** bearing USN **1KS21EC075** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Ritesh Kumar Sinha** bearing USN **1KS21EC076** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Rithika M** bearing USN **1KS21EC077** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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INTERNSHIP CERTIFICATE

This is to certify that **Mr. S Hari Dhanush** bearing USN **1KS21EC078** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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Dr P N Sudha
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INTERNSHIP CERTIFICATE

This is to certify that **Mr. S Shajith Ali** bearing USN **1KS21EC080** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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INTERNSHIP CERTIFICATE

This is to certify that **Mr. Sagar G S** bearing USN **1KS21EC081** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Sai Rahul N** bearing USN **1KS21EC082** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Samhitha Prakash** bearing USN **1KS21EC083** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Sanjana V** bearing USN **1KS21EC084** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Sanjay G** bearing USN **1KS21EC085** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Sanjay N** bearing USN **1KS21EC086** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Sanjay P** bearing USN **1KS21EC087** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Sathyam Kumar Mandal S** bearing USN **1KS21EC088** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Shaik Arfath** bearing USN **1KS21EC089** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Shashank C U** bearing USN **1KS21EC090** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Shreyas Raghavendra V** bearing USN **1KS21EC091** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Shwetha V** bearing USN **1KS21EC092** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Sindhu M Nimbal** bearing USN **1KS21EC093** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Spoorthy M U** bearing USN **1KS21EC095** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Srilakshmi G** bearing USN **1KS21EC096** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Sripriya H G** bearing USN **1KS21EC097** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Sumukh P** bearing USN **1KS21EC098** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Suneetha** bearing USN **1KS21EC099** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Suneha S** bearing USN **1KS21EC100** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Supreeth A** bearing USN **1KS21EC101** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Surabhi K R** bearing USN **1KS21EC102** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Sushen Krishnapur** bearing USN **1KS21EC103** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Tarun M** bearing USN **1KS21EC104** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Tejashree N** bearing USN **1KS21EC105** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Tharun K V** bearing USN **1KS21EC106** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Thejas H V** bearing USN **1KS21EC107** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Thushar Cherian** bearing USN **1KS21EC108** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Udaya Kumar S R** bearing USN **1KS21EC109** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Vaishnavi B A** bearing USN **1KS21EC110** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Varsha Jayakumar** bearing USN **1KS21EC111** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Varsha S Davaskar** bearing USN **1KS21EC112** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Varshith S** bearing USN **1KS21EC113** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Veeresh K N** bearing USN **1KS21EC114** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Vidya I** bearing USN **1KS21EC115** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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INTERNSHIP CERTIFICATE

This is to certify that **Ms. Vidya Rawal D** bearing USN **1KS21EC116** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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Dr P N Sudha
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This is to certify that **Ms. Vidyashree R** bearing USN **1KS21EC117** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Vijay Yadav R** bearing USN **1KS21EC118** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Vyshak G R** bearing USN **1KS21EC120** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Yashwanth.M** bearing USN **1KS21EC121** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Adithya D** bearing USN **1KS22EC400** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Apoorva B** bearing USN **1KS22EC401** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. B Sreepadreddi H Bullangoudar** bearing USN **1KS22EC402** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Chaitra N** bearing USN **1KS22EC403** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Gonuguntla Shrujana** bearing USN **1KS22EC404** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Hema K** bearing USN **1KS22EC405** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Pavangowda H P** bearing USN **1KS22EC406** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Prajwal Patil B S** bearing USN **1KS22EC407** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Sangeetha H M** bearing USN **1KS22EC408** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Soundarya S** bearing USN **1KS22EC409** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Sowmya A M** bearing USN **1KS22EC410** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Mr. Sudeep P** bearing USN **1KS22EC411** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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This is to certify that **Ms. Vaishanavi V** bearing USN **1KS22EC412** has successfully completed the Internship on '**Simulation of Analog & Digital Circuits using Multisim**' organized by the **Department of Electronics & Communication Engineering**, K S Institute of Technology, Bengaluru from 17/10/2022 to 21/10/2022.

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